

VP1

Plastic Package 5.0 Vdc VCXO



1 to 44.736 MHz

+5.0 Vdc  $\pm 10\%$

HCMOS/TTL

Low profile 4.7 mm/0.19 inch tall

J Leaded Surface mount

TYPICAL P/N VP1- **K 1** 27 MHz

**1** = 50/50  $\pm 10\%$  Symmetry

**K** =  $\pm 50$  ppm 0 to +70°C

FREQUENCY: 1 to 44.736 MHz

STABILITY OPTIONS<sup>1</sup>:

<b>I</b> $\pm 25$ ppm 0 to +70°C	<b>J</b> $\pm 25$ ppm -40 to +85°C
<b>K</b> $\pm 50$ ppm 0 to +70°C	<b>L</b> $\pm 50$ ppm -40 to +85°C

STORAGE TEMP: -55°C to +125°C

SYMMETRY<sup>2</sup> / LINEARITY

<b>1</b> = 50/50 $\pm 10\%$	with $\pm 20\%$ Linearity
<b>2</b> = 50/50 $\pm 5\%$	with $\pm 20\%$ Linearity
<b>3</b> = 50/50 $\pm 10\%$	with $\pm 5\%$ Linearity
<b>4</b> = 50/50 $\pm 5\%$	with $\pm 5\%$ Linearity

1) Includes calibration tolerance, load and supply variations. VC is positive slope.

2) TTL symmetry measured at 1.4 Vdc. HCMOS measured at  $\frac{1}{2}$  wave amplitude.

Pullability:  $\pm 100$  ppm minimum (<40 MHz)  
0.5 to 4.5 Vdc  $\pm 75$  ppm minimum (>40 MHz)

AGING: 5 ppm/year Max

TTL LOAD: 10 loads max

TTL LOGIC "0": 0.4 Vdc max

TTL LOGIC "1": 2.4 Vdc minimum

HCMOS LOAD: 50 pF maximum

HCMOS LOGIC: Logic "0" 0.5 Vdc Max.  
Logic "1" 4.5 Vdc Min.

INPUT CURRENT: <20 mA to 20 MHz

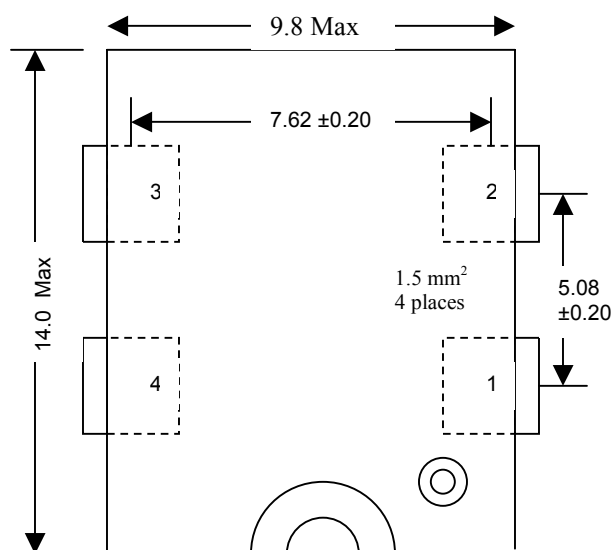
<50 mA to 40 MHz

Tr and Tf <6 ns maximum

PACKAGE COMPARABLE TO:

Epson SG615G & SG8002JA or  
Ecliptek EC14

## LAND PATTERN



Dimensions in mm. TOP VIEW. Pad dimensions are center to center. Not to scale. J-lead width is 0.5 mm.

PAD 1 = Voltage Control  
PAD 2 = Ground  
PAD 3 = Output  
PAD 4 = +5.0 Vdc