

■ Features

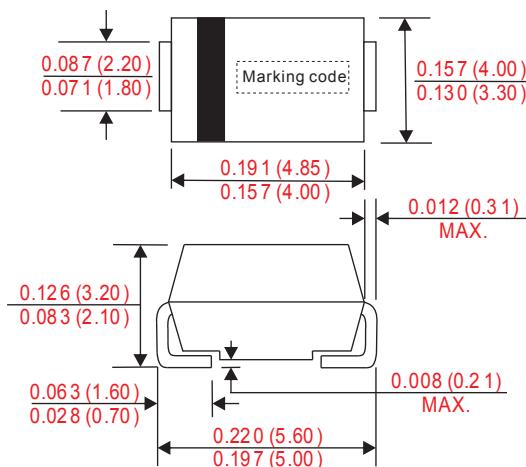
- Electrostatic discharge (ESD) test under IEC6100-4-2 standard >16KV(SS32BL~SS36BL). standard >10KV(SS310BL~SS320BL).
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction. Suffix "H" indicates Halogen-free part, ex.SS32BLH.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AA / SMB
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : 0.003 ounce, 0.091 gram

■ Outline

SMB(DO-214AA)



Dimensions in inches and (millimeters)

■ Maximum ratings and electrical characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	I _O			3.0	A
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I _{FSM}			125	A
Reverse current	V _R = V _{RRM} T _A = 25°C	I _R	0.5	20	mA	
	V _R = V _{RRM} T _A = 100°C					
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C _J	250			pF
Thermal resistance	Junction to ambient	R _{θJA}	55			°C/W
Storage temperature		T _{STG}	-55		+175	°C

Symbol	Marking code	Max. repetitive peak reverse voltage V _{RRM} (V)	Max. RMS voltage V _{RMS} (V)	Max. DC blocking voltage V _R (V)	Max. forward voltage @3A, T _A = 25°C V _F (V)	Operating temperature T _J (°C)
SS32BL	SS32L	20	14	20	0.40	-55 ~ +150
SS34BL	SS34L	40	28	40	0.45	
SS36BL	SS36L	60	42	60	0.55	
SS310BL	SS310L	100	70	100	0.65	
SS315BL	SS315L	150	105	150	0.82	
SS320BL	SS320L	200	140	200	0.85	

■ Rating and characteristic curves

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

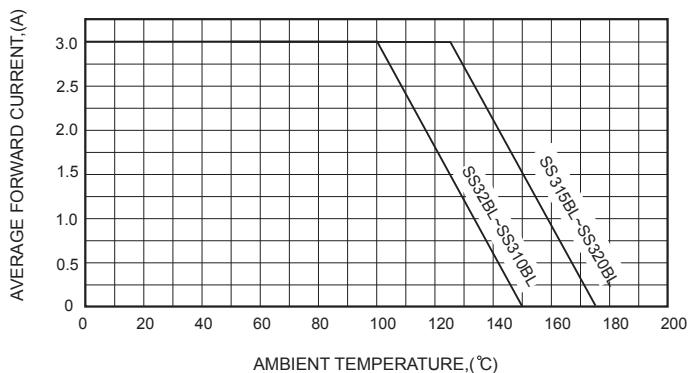


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

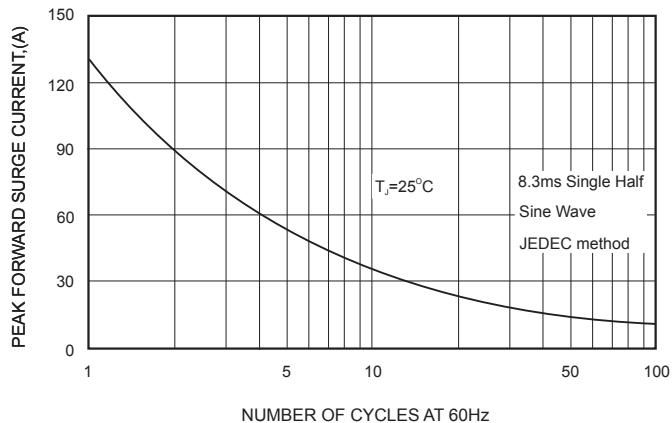


FIG.4-TYPICAL JUNCTION CAPACITANCE

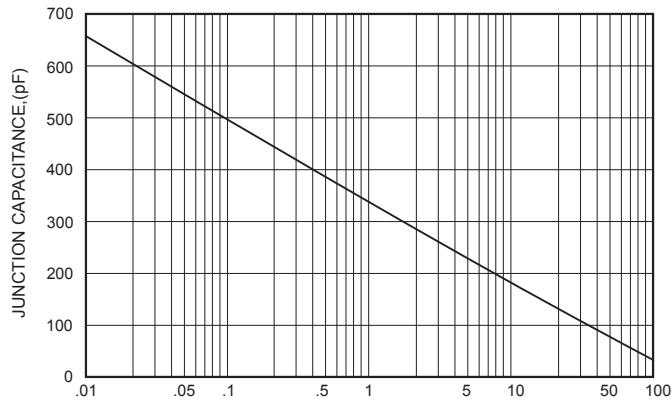


FIG.2-TYPICAL FORWARD CHARACTERISTICS

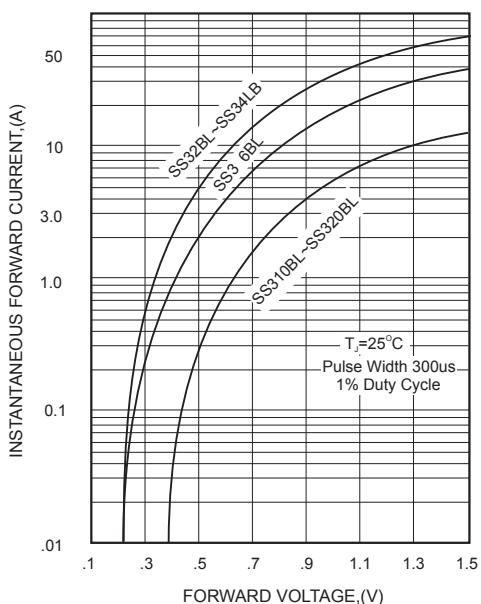


FIG.5 - TYPICAL REVERSE CHARACTERISTICS

